

Verification Condition Generation and Variable Conditions in Smallfoot

Josh Berdine¹, Cristiano Calcagno², and Peter W. O'Hearn³

¹ Microsoft Research

² ETH Zurich, Imperial College London, and Monoidics Ltd

³ University College London

Abstract. These notes are a companion to [1] which describe

- the variable conditions that Smallfoot checks,
- the analysis used to check them,
- the algorithm used to compute a set of verification conditions corresponding to an annotated program, and
- the treatment of concurrent resource initialization code.

2012 Introduction

This document presents the variable conditions and checking algorithms as implemented in Smallfoot 0.1 of late 2005. These conditions on the proof rules for concurrency rely on some of the relaxations introduced by Brookes [3] relative to O'Hearn's system [7], and so originally soundness was via Brookes's result. Ian Wehrman and Berdine have since found some cases where these relaxed conditions are unsound, prompting a revisitation of this topic. Brookes [4] and Reddy and Reynolds [9] have recently introduced systems which address these issues while admitting encodings of proofs in O'Hearn's more restrictive system (among other improvements).

In hindsight, while Smallfoot needs more than O'Hearn's system, it does not use the full relaxation of Brookes's original system, in particular retaining concurrency condition 5 below. As a result, the proofs found by Smallfoot appear to be embeddable into either of the recently proposed sound systems, although we do not claim to have a formal proof at this time.

The condition checking algorithms remain non-compositional, and hence uses a whole-program analysis, despite the compositionality of both Brookes's revised system and that of Reddy and Reynolds. This is a result of the necessity of guiding the search for a proof to one which satisfies the occurrence conditions, as opposed to the distinct problem of checking whether a given candidate proof outline satisfies the conditions, or of inferring a valid permissions annotation as in [9].

1 Checking Variable Conditions

1.1 Annotated Programs

Each Smallfoot program determines a resource environment Γ which contains the resource declarations

$$r_i(\vec{x}_i)R_i$$

where \vec{x}_i and R_i are resource r_i 's protected variables and invariant; and a procedure environment Δ which contains the procedure declarations

$$f(\vec{p}; \vec{v})[P_f] C_f [Q_f]$$

where procedure f 's parameters \vec{p} are passed by reference and \vec{v} by value, and assertions P_f and Q_f are f 's pre- and post-conditions. We assume that Γ and Δ are given.

Commands are generated by:

$$\begin{aligned} E &::= x \mid \text{nil} \mid c \mid E \text{ xor } E \\ B &::= E=E \mid E \neq E \\ S &::= x:=E \mid x:=E \rightarrow t \mid E \rightarrow t:=E \mid x:=\text{new}() \mid \text{dispose}(E) \\ C &::= S \mid C; C \mid \text{if}(B) \{C\} \text{ else } \{C\} \mid \text{while}(B) [I] \{C\} \\ &\quad \mid f(\vec{x}; \vec{E}) \mid f(\vec{x}; \vec{E}) \parallel f(\vec{x}; \vec{E}) \mid \text{with } r \text{ when}(B) \{C\} \end{aligned}$$

1.2 Legal Annotated Programs

Using the following notation

$$\begin{aligned} \text{owned}(\vec{r}) &\stackrel{\text{def}}{=} \bigcup_{i.r_i \in \vec{r}} \vec{x}_i \\ \text{var}(\vec{r}) &\stackrel{\text{def}}{=} \text{owned}(\vec{r}) \cup \bigcup_{i.r_i \in \vec{r}} \text{fv}(R_i) \end{aligned}$$

where \vec{r} denotes a set of resources, the set of legal annotated programs is restricted by the following constraints:

- In any procedure call $f(\vec{y}; \vec{E})$ or region $\text{with } r \text{ when}(B) \{C\}$ the variable f/r must be defined in a procedure/resource declaration.
- In every procedure declaration $f(\vec{p}; \vec{v})[P_f] C_f [Q_f]$, the formal parameters \vec{p}, \vec{v} are all distinct.
- The resources \vec{r} must be distinct.
- The protection lists must all be disjoint: $\text{owned}(r_i) \cap \text{owned}(r_j) = \emptyset$ when $i \neq j$.
- No resource's invariant may have a free occurrence of a variable in a distinct resource's protection list: $\text{fv}(R_i) \cap \vec{x}_j = \emptyset$ when $i \neq j$.

1.3 Simplifying Assumptions

Additionally, we assume a pre-processing phase which renames bound variables to satisfy the following simplifying assumptions:

- Bound variables (formal parameters and variables bound by `local`) are distinct from one another, and from global variables.
- For each procedure declaration, variables in the postcondition are not bound (by `local`) in the body.

1.4 Variable Conditions

We define functions $\text{var}(-)$, $\text{mod}(-)$, $\text{req}(-)$ on commands C and procedure names f . Intuitively:

- $\text{var}(C)$ is set of variables that C mentions (in the program or specifications, recursively) without protection;
- $\text{mod}(C)$ is the set of variables that C may modify without protection (acquiring r protects $\text{owned}(\{r\})$);
- $\text{req}(C)$ is the set of resources required to be acquired before executing C .

The auxiliary function $\text{er}(M, A)$, returning the set of resources that need to be acquired before modifying variables in M and accessing variables in A , is defined as:

$$\text{er}(M, A) = \left\{ r \mid \begin{array}{l} r(\vec{x})R \in \Gamma \text{ and} \\ (A \cap \vec{x} \neq \emptyset \text{ or } M \cap (\vec{x} \cup \text{fv}(R)) \neq \emptyset) \end{array} \right\}$$

The definition proceeds by performing a simple fixpoint calculation to determine the least solution (which is best) of the following equations, ordering by pointwise subset inclusion of functions:

$$\begin{aligned} \text{var}(f) &= (\text{var}(C) \cup \text{fv}(P, Q)) - (\vec{p} \cup \vec{v}) \\ \text{mod}(f) &= \text{mod}(C) - (\vec{p} \cup \vec{v}) \\ \text{req}(f) &= \text{req}(C) \cup \text{er}(\emptyset, \text{fv}(P, Q) - (\vec{p} \cup \vec{v})) \end{aligned}$$

where $f(\vec{p}; \vec{v})[P] C [Q] \in \Delta$, and for commands:

$$\begin{aligned} \text{var}(x := E) &= \{x\} \cup \text{fv}(E) \\ \text{var}(x := E \rightarrow t) &= \{x\} \cup \text{fv}(E) \\ \text{var}(E \rightarrow t := F) &= \text{fv}(E) \cup \text{fv}(F) \\ \text{var}(x := \text{new}()) &= \{x\} \\ \text{var}(\text{dispose}(E)) &= \text{fv}(E) \\ \text{var}(C ; C') &= \text{var}(C) \cup \text{var}(C') \\ \text{var}(\text{if}(B) \{C\} \text{ else } \{C'\}) &= \text{fv}(B) \cup \text{var}(C) \cup \text{var}(C') \\ \text{var}(\text{while}(B) [I] \{C\}) &= \text{fv}(I, B) \cup \text{var}(C) \end{aligned}$$

$$\begin{aligned}
\text{var}(f(\vec{x}; \vec{E})) &= \text{var}(f) \cup \vec{x} \cup \text{fv}(\vec{E}) \\
\text{var}(f(\vec{x}; \vec{E}) \parallel f'(\vec{x}'; \vec{E}')) &= \text{var}(f(\vec{x}; \vec{E})) \cup \text{var}(f'(\vec{x}'; \vec{E}')) \\
\text{var}(\text{with } r \text{ when}(B) \{C\}) &= ((\text{fv}(B) \cup \text{var}(C)) - \text{fv}(R_r)) \cup (\text{mod}(C) - \text{owned}(\{r\}))
\end{aligned}$$

$$\begin{aligned}
\text{mod}(x := E) &= \{x\} \\
\text{mod}(x := E \rightarrow t) &= \{x\} \\
\text{mod}(E \rightarrow t := F) &= \emptyset \\
\text{mod}(x := \text{new}()) &= \{x\} \\
\text{mod}(\text{dispose}(E)) &= \emptyset \\
\text{mod}(C; C') &= \text{mod}(C) \cup \text{mod}(C') \\
\text{mod}(\text{if}(B) \{C\} \text{ else } \{C'\}) &= \text{mod}(C) \cup \text{mod}(C') \\
\text{mod}(\text{while}(B) [I] \{C\}) &= \text{mod}(C) \\
\text{mod}(f(\vec{x}; \vec{E})) &= \text{mod}(f) \cup \vec{x} \\
\text{mod}(f(\vec{x}; \vec{E}) \parallel f'(\vec{x}'; \vec{E}')) &= \text{mod}(f(\vec{x}; \vec{E})) \cup \text{mod}(f'(\vec{x}'; \vec{E}')) \\
\text{mod}(\text{with } r \text{ when}(B) \{C\}) &= \text{mod}(C) - \text{owned}(\{r\})
\end{aligned}$$

$$\begin{aligned}
\text{req}(S) &= \text{er}(\text{mod}(S), \text{var}(S)) \\
\text{req}(C; C') &= \text{req}(C) \cup \text{req}(C') \\
\text{req}(\text{if}(B) \{C\} \text{ else } \{C'\}) &= \text{req}(C) \cup \text{req}(C') \cup \text{er}(\emptyset, \text{fv}(B)) \\
\text{req}(\text{while}(B) [I] \{C\}) &= \text{req}(C) \cup \text{er}(\emptyset, \text{fv}(I, B)) \\
\text{req}(f(\vec{x}; \vec{E})) &= \text{req}(f) \cup \text{er}(\vec{x}, \text{fv}(\vec{E})) \\
\text{req}(f(\vec{x}; \vec{E}) \parallel f'(\vec{x}'; \vec{E}')) &= \text{req}(f(\vec{x}; \vec{E})) \cup \text{req}(f'(\vec{x}'; \vec{E}')) \\
\text{req}(\text{with } r \text{ when}(B) \{C\}) &= (\text{req}(C) \cup \text{er}(\emptyset, \text{fv}(B))) - \{r\}
\end{aligned}$$

The definitions of *var* and *mod* are as expected, except that CCR statements *with* *r when*(*B*) {*C*} hide accesses and modifications of the variables *owned*(*r*) in *C*. Note that variables in *fv*(*R_r*) can only be modified in a critical region for *r*. Therefore, when computing the external effect of a command *with* *r when*(*B*) {*C*} we can ignore the reads to *fv*(*R_r*) since they can never happen in parallel with a write. For *req*, any mention of a variable protected by *r*, or modification of a variable in *r*'s invariant, causes *r* to be required; and a CCR for *r* discharges the requirement of *r*.

Variable Aliasing Conditions The variable conditions for aliasing follow those of [6] and [5]. The conditions needed to avoid variable (not heap) aliasing are enforced by checking, for every procedure call $f(\vec{x}; \vec{E})$ in the program:

- The actual reference parameters \vec{x} are distinct.
- If a global variable *z* is passed by reference, then *f* and procedures *f* calls, recursively, must not read or modify *z*, or mention it in specifications: $\vec{x} \cap \text{var}(f) = \emptyset$.

Variable Conditions for Concurrency The variable conditions for concurrency follow those of [8, 7, 3]. The first two concurrency conditions:

1. Protected variables of r appear only within CCRs for r .
2. Variables appearing in a resource r 's invariant can only be modified within CCRs for r .

are checked using the computed $\text{req}(-)$. A violation of one of these conditions results in “too large” a required resources set for the offending code, which eventually propagates to the main procedure. Hence, we check that $\text{req}(\text{main}) = \emptyset$, if it appears. Note that this analysis ignores deadlock due to acquiring an already-held resource, as Smallfoot only proves safety.

The third and fourth concurrency conditions:

3. Only protected variables can be modified in one parallel process and read or mentioned in specifications in another.
4. For each parallel composition $f(\vec{x}; \vec{E}) \parallel f'(\vec{x}'; \vec{E}')$, $f(\vec{x}; \vec{E})$ and the specification of f cannot mention variables modified by $f'(\vec{x}'; \vec{E}')$, and vice versa.

are checked using the computed $\text{var}(-)$ and $\text{mod}(-)$:

$$\begin{aligned} & \text{mod}(f(\vec{x}; \vec{E})) \cap (\text{fv}(P', Q') \cup \text{var}(f'(\vec{x}'; \vec{E}'))) = \emptyset \\ & \text{and } \text{mod}(f'(\vec{x}'; \vec{E}')) \cap (\text{fv}(P, Q) \cup \text{var}(f(\vec{x}; \vec{E}))) = \emptyset, \\ & \text{where } f(\vec{p}; \vec{v})[P] C [Q], f'(\vec{p}'; \vec{v}') [P'] C' [Q'] \in \Delta. \end{aligned}$$

The final concurrency condition is a property of program proofs, not of annotated programs themselves:

5. Whenever a CCR is symbolically executed, the pre and post states cannot mention variables modified by other processes.

Note that according to the inference rule for CCRs, entering a CCR adds the resource invariant to the current precondition. Also, it may be that processes running in parallel with the one executing the CCR under consideration modify variables appearing in the added invariant. For this reason, we introduce a further analysis which computes, for each procedure f , the set $\text{par}(f)$ of procedures that might run in parallel with f :

1. $\text{par}(f) \supseteq \{f'\}$ for all occurrences of $f(\vec{x}; \vec{E}) \parallel f'(\vec{x}'; \vec{E}')$ or $f'(\vec{x}'; \vec{E}')$ in the program;
2. $\text{par}(f') \supseteq \text{par}(f)$ for all occurrences of $f'(\vec{x}'; \vec{E}')$ or $f'(\vec{x}'; \vec{E}') \parallel f''(\vec{x}''; \vec{E}'')$ or $f''(\vec{x}''; \vec{E}'') \parallel f'(\vec{x}'; \vec{E}')$ in C_f .

(As before, we take the smallest set satisfying the above conditions.) The results of this analysis are then used in VCGen to instrument the VCs for CCRs so that any such variables modified by processes in parallel are quantified out of the post states of CCRs during symbolic execution, thereby avoiding bad proofs.

Checking the concurrency conditions essentially classifies each variable into one of the following five classes:

Local variables are declared by `local`, or are procedure value parameters. Their use is unrestricted within their scope.

Process-local variables appear, and are mentioned in specifications, in only one process, and do not appear in any resource invariants. In that process, their use is unrestricted.

Global-constant variables appear in some function and are not local. They cannot be written to but can be read or appear in specifications, including resource invariants, in any process.

Protected variables are those which appear in one resource’s protection list, and can be modified, accessed, or mentioned in specifications in any process, but only within critical regions for the associated resource.

Process-protected variables appear in at least one resource invariant and in only one process. In that process they are modified only within critical regions for all the resources in whose invariants they appear. Also in that process, they can be read and appear in specifications outside of critical regions. Variables which appear free in some resource invariant but are not protected are either process-protected or global-constant, depending on whether they are ever written to.

2 Verification Condition Generation

2.1 Verification Conditions

A verification condition is a triple $[P] SI [Q]$ where SI is a “symbolic instruction”:

$$SI ::= \epsilon \mid S \mid [P] \text{jsr}_{\vec{x}} [Q] \mid \text{if } B \text{ then } SI \text{ else } SI \mid SI ; SI$$

A symbolic instruction is a piece of loop-free sequential code where all procedure calls have been instantiated to `jsr` instructions of the form $[P] \text{jsr}_{\vec{x}} [Q]$. This form plays a central role in Smallfoot. We use it not only to handle procedure calls, but also for concurrency and for entry to and exit from a critical region.

Semantically, $[P] \text{jsr}_{\vec{x}} [Q]$ is a “generic command” in the sense of [10]. It is the greatest relation satisfying the pre- and post-condition, and subject to the constraint that only the variables in \vec{x} are modified.

The symbolic execution rule for the `jsr` instruction is:

$$\frac{\Pi \wedge \Sigma \vdash \Pi' \wedge \Sigma' * \Sigma_F \quad [(\Pi[\vec{x}'/\vec{x}]) \wedge Q * (\Sigma_F[\vec{x}'/\vec{x}])] C [R]}{[\Pi \wedge \Sigma] ([\Pi' \wedge \Sigma'] \text{jsr}_{\vec{x}} [Q] ; C) [R]} \vec{x}' \text{ fresh}$$

To apply this rule we have to discover a frame axiom Σ_F which describes the portion of heap unchanged by a procedure call, and [2] describes a proof-theoretic method for obtaining them.

2.2 VCGen

For each procedure declaration $f(\vec{p}; \vec{v})[P] C [Q]$ we generate a set of verification conditions $\text{vcg}(f, [P] C [Q])$, which is itself defined using a helper function *chop*

that takes a command and produces a symbolic instruction together with a set of verification conditions. *vcg* just runs *chop* on the body *C*, tacks the pre and post onto the resulting symbolic instruction, and adds that to the verification conditions returned by *chop*.

$$\begin{aligned}
vcg(g, [P] C [Q]) &= \{[P] SI [Q]\} \cup L \\
&\text{where } SI, L = chop(g, C) \\
chop(g, S) &= S, \emptyset \\
chop(g, C; C') &= SI; SI', L' \cup L' \\
&\text{where } SI, L = chop(g, C) \text{ and } SI', L' = chop(g, C') \\
chop(g, \text{if}(B) \{C\} \text{ else } \{C'\}) &= \text{if } B \text{ then } SI \text{ else } SI', L \cup L' \\
&\text{where } SI, L = chop(g, C) \text{ and } SI', L' = chop(g, C') \\
chop(g, \text{while}(B) [I] \{C\}) &= [I] \text{jsr}_{mod(C)} [\neg B \wedge I], vcg([B \wedge I] C [I]) \\
chop(g, f(\vec{x}; \vec{E})) &= ([\text{emp}] \text{jsr}_{\emptyset} [\vec{v}' = \vec{E} \wedge \text{emp}]; [P[\vec{x}/\vec{p}, \vec{v}'/\vec{v}]] \text{jsr}_{mod(C_f)[\vec{x}/\vec{p}, \vec{v}'/\vec{v}]} [Q[\vec{x}/\vec{p}, \vec{v}'/\vec{v}]]), \emptyset \\
&\text{where } f(\vec{p}; \vec{v})[P] C [Q] \text{ and } \vec{v}' \text{ fresh} \\
chop(g, f(\vec{x}; \vec{E}) \parallel f'(\vec{x}'; \vec{E}')) &= ([\text{emp}] \text{jsr}_{\emptyset} [\vec{v} = \vec{E} \wedge \vec{v}' = \vec{E}' \wedge \text{emp}]; [P * P'] \text{jsr}_{\vec{z} \cup \vec{z}'} [Q * Q']), \emptyset \\
&\text{where } [\text{emp}] \text{jsr}_{\emptyset} [\vec{v} = \vec{E} \wedge \text{emp}]; [P] \text{jsr}_{\vec{z}} [Q], \emptyset = chop(g, f(\vec{x}; \vec{E})) \\
&\quad [\text{emp}] \text{jsr}_{\emptyset} [\vec{v}' = \vec{E}' \wedge \text{emp}]; [P'] \text{jsr}_{\vec{z}'} [Q'], \emptyset = chop(g, f'(\vec{x}'; \vec{E}')) \\
chop(g, \text{with } r \text{ when}(B) \{C\}) &= ([\text{true} \wedge \text{emp}] \text{jsr}_{\emptyset} [B \wedge R]; SI; [\text{true} \wedge R] \text{jsr}_{\vec{x} \cup \vec{u}} [\text{true} \wedge \text{emp}]), L \\
&\text{where } SI, L = chop(g, C) \text{ and } r(\vec{x})R \text{ and } \vec{u} = \text{fv}(R) \cap \bigcup_{f \in \text{par}(g)} mod(f)
\end{aligned}$$

The definition of *chop* for primitive statements, sequential composition, conditionals and loops is mostly as expected, except that for loops we generate a *jsr* instruction that allows invariants to be smaller than they might otherwise be, because of framing.

For procedure call, we rename the value parameters and use two *jsr*'s: the first to initialize the renamed parameters and the second to abstract the body of the procedure, using only its spec. This renaming allows the postcondition to refer to the initial value of the parameters which are not modified by the body. The composition of the two *jsr*'s satisfies a spec $[A] - [B]$ iff the second one satisfies $[A \wedge \vec{v}' = \vec{E}] - [B]$. In the definition, $mod(C_f)$ is the set of variables modified by C_f (or one of the procedures that C_f calls) except for protected variables modified within a CCR.

For parallel composition we emit two *jsr*'s that combine the initializations of the two procedure calls, and take the $*$ -combinations of the respective preconditions and postconditions, following the parallel proof rule

$$\frac{[P] C [Q] \quad [P'] C' [Q']}{[P * P'] C \parallel C' [Q * Q']}.$$

Entry to, and exit from, CCRs is modeled by *jsr* instructions. The entry *jsr* adds the resource invariant and boolean condition to the symbolic state. Since

this represents adding *any* concrete heap satisfying the invariant and condition, upon entry to a CCR the body cannot assume or depend on anything further about the acquired heap. This is how we handle potential interference from parallel processes, which may change one concrete heap satisfying the invariant to another. Additionally, outside interference is prevented in code following a CCR since the exit `jsr` removes the resource invariant from the symbolic state and forgets the values of variables which are protected, \vec{x} , or might be modified by processes $\text{par}(f)$ running in parallel, \vec{u} . The net result is that correctness of a parallel program is reduced to several sequential triples, and no interleaving needs to be considered. This VC definition follows the description of the CCR proof rule

$$\frac{[(P * R_r) \wedge B] C [Q * R_r]}{[P] \text{ with } r \text{ when}(B) \{C\} [Q]}$$

(where R_r is an invariant formula associated with resource r) and both occurrences of `jsr` make use of the frame axiom inference capability; the precondition P of a CCR is maintained after the entry `jsr`, and an appropriate Q part for the postcondition in the rule is discovered as a frame axiom for the exit `jsr`. The $r(\vec{x})R$ in the where clause indicates that R is the declared invariant of r in the program.

To tie all of this together there is one further check that must be made. The *init* procedure must establish all of the resource invariants, separately, and the precondition of *main* if a main procedure is included. So given `init()` $[P] C [Q]$ and `main()` $[P'] C' [Q']$ we check the entailment $Q \vdash R_1 * \dots * R_n * P'$. We also require C to not contain procedure calls, CCRs, or parallel compositions. All told, the property that this establishes (following the rule for complete programs [7, 3]) for a program is

$$[P] C ; \text{RESDECLS} ; \text{let PROCDECLS in } C' [Q' * R_1 * \dots * R_n]$$

where PROCDECLS consists of those procedure declarations other than *main* and *init*. (We could also include a finalization procedure that disposes of the R_i at the end.)

3 Resource Initialization

Resource initializers are subject to the following constraints:

1. No resource's initializer modifies a variable mentioned by a distinct resource:
 $\text{mod}(C_i) \cap \text{var}(r_j) = \emptyset$ when $i \neq j$.
This is performed by checking, for all i

$$\begin{aligned} \text{mod}(C_i) \cap \text{var}(C_1, \dots, C_{i-1}) &= \emptyset \\ \text{and } \text{var}(C_i) \cap \text{mod}(C_1, \dots, C_{i-1}) &= \emptyset \end{aligned}$$

2. The `init` procedure, if it appears, and the resource initializers C_i contain no procedure calls or CCRs.

These constraints ensure that the order in which the initializers are executed is immaterial. Therefore, we have an additional verification condition:

$$\text{vcg}([P] C [Q * R_1 * \dots * R_n])$$

where R_1, \dots, R_n are all the resource invariants and

$$P, C, Q = \begin{cases} P', C', Q' & \text{if } \text{init}()[P'] C' [Q'] \in \Delta \\ \text{emp}, C_1; \dots; C_n, \text{emp} & \text{otherwise} \end{cases}$$

where C_1, \dots, C_n are all the resource initializers (in some unspecified order).

Also, in case `init` appears, the precondition of procedure `main`, if it appears, is taken to be the postcondition of `init`, irrespective of what appears in the file.

References

1. J. Berdine, C. Calcagno, and P. W. O'Hearn. Smallfoot: Modular automatic assertion checking with separation logic. In *FMCO*, 2005.
2. J. Berdine, C. Calcagno, and P. W. O'Hearn. Symbolic execution with separation logic. In *APLAS*, 2005.
3. S. Brookes. A semantics for concurrent separation logic. *Theor. Comput. Sci.*, 2007. Preliminary version in *CONCUR'04*.
4. S. Brookes. A revisionist history of concurrent separation logic. *Electr. Notes Theor. Comput. Sci.*, 2011.
5. S. A. Cook. Soundness and completeness of an axiomatic system for program verification. *SIAM J. on Computing*, 1978.
6. C. Hoare. Procedures and parameters: An axiomatic approach. In *Symposium on the Semantics of Algorithmic Languages*, 1971.
7. P. W. O'Hearn. Resources, concurrency, and local reasoning. *Theor. Comput. Sci.*, 2007. Preliminary version in *CONCUR'04*.
8. S. Owicki and D. Gries. Verifying properties of parallel programs: An axiomatic approach. *CACM*, 1976.
9. U. S. Reddy and J. C. Reynolds. Syntactic control of interference for separation logic. In *POPL*, 2012.
10. J. Schwarz. Generic commands—A tool for partial correctness formalisms. *The Computer Journal*, 1977.